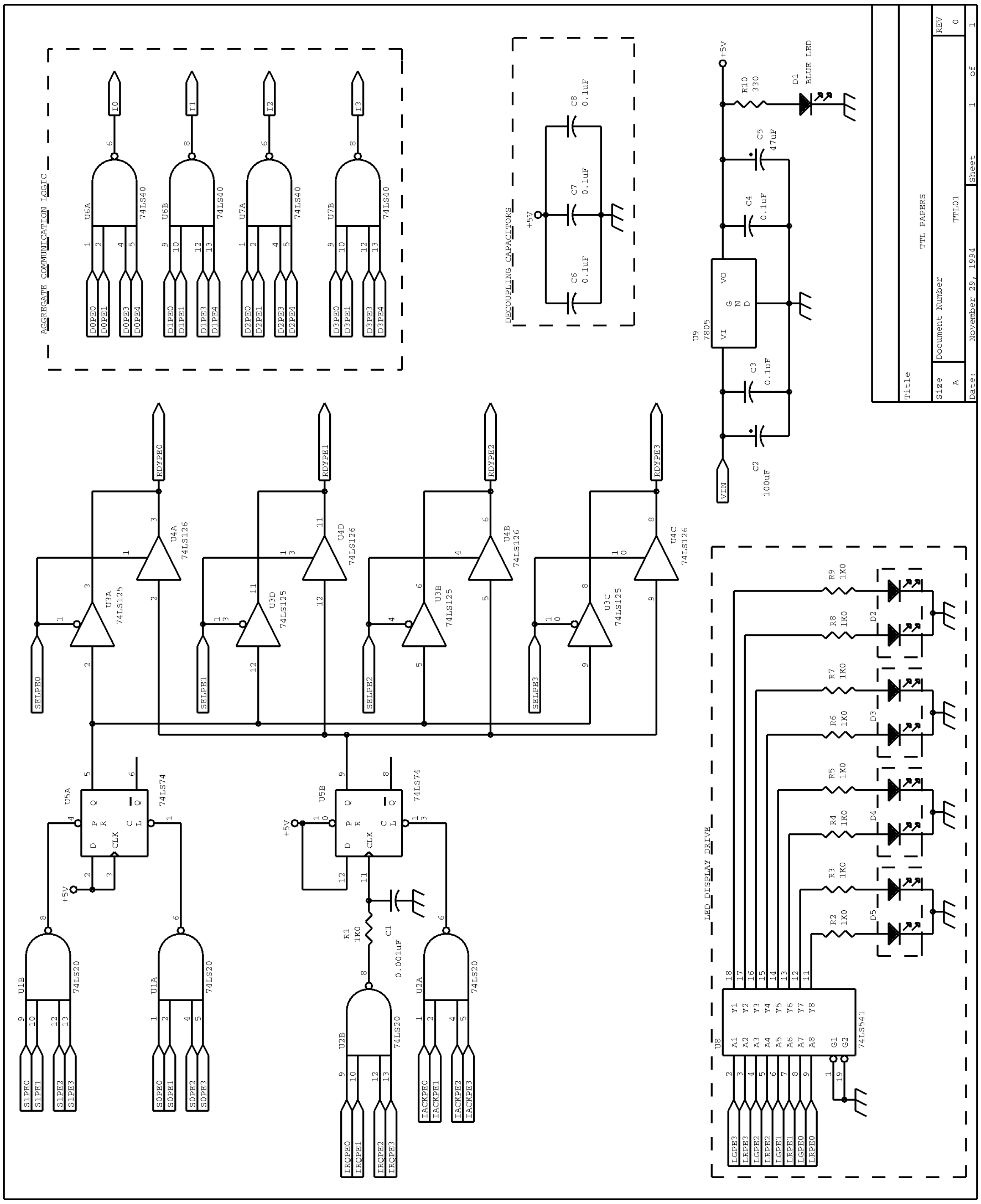

Figure 2: TTL_PAPERS Circuit Diagram